

## EAST Search History

| Ref # | Hits | Search Query  | DBs   | Default Operator | Plurals | Time Stamp       |
|-------|------|---|---|------------------|---------|------------------|
| S1    | 1    | (processor AND instruction register AND switchover device AND pseudorandom number generating device AND defined instruction AND undefined instruction AND normal operation AND test operation).clm. | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | ADJ              | ON      | 2006/11/02 13:34 |
| S2    | 2    | processor\$1 same (instruction register\$1 same input switchover device)  | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | ADJ              | ON      | 2006/11/02 13:46 |
| S3    | 2    | output\$4 and (pseudorandom number\$1 same defined instruction)   | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | ADJ              | ON      | 2006/11/02 14:53 |
| S4    | 2    | convert\$3 and (pseudorandom number\$1 same undefined instruction)  | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | ADJ              | ON      | 2006/11/02 14:50 |
| S5    | 1    | output\$4 pseudorandom number\$1 and defined instruction  | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | ADJ              | ON      | 2006/11/02 14:50 |
| S6    | 1    | convert\$3 pseudorandom number\$1 and undefined instruction   | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | ADJ              | ON      | 2006/11/02 14:51 |
| S7    | 4    | pseudorandom number generating device   | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | ADJ              | ON      | 2006/11/02 14:54 |

## EAST Search History

|     |         |                                |   |     |    |                  |
|-----|---------|--------------------------------|---|-----|----|------------------|
| S8  | 17      | scan shift control\$4          | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | ADJ | ON | 2006/11/02 14:54 |
| S9  | 926     | pseudorandom number generat\$3 | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | ADJ | ON | 2006/11/02 14:54 |
| S10 | 11962   | instruction register\$1        | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | ADJ | ON | 2006/11/02 14:54 |
| S11 | 2970    | instruction convert\$3         | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | ADJ | ON | 2006/11/02 14:55 |
| S12 | 86      | input switchover               | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | ADJ | ON | 2006/11/02 14:55 |
| S13 | 1647    | pseudorandom number\$1         | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | ADJ | ON | 2006/11/02 14:57 |
| S14 | 1057262 | processor\$1                   | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | ADJ | ON | 2006/11/02 14:58 |
| S15 | 2       | S14 same S10 same S12          | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | ADJ | ON | 2006/11/02 14:59 |

## EAST Search History

|     |       |   |   |     |    |                  |
|-----|-------|---|---|-----|----|------------------|
| S16 | 21    | S13 and S14 and S10   | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | ADJ | ON | 2006/11/02 14:59 |
| S17 | 21    | S16 and S9  | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | ADJ | ON | 2006/11/02 15:32 |
| S18 | 2     | S17 and S11   | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | ADJ | ON | 2006/11/02 15:00 |
| S19 | 2     | S17 and S12   | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | ADJ | ON | 2006/11/02 15:01 |
| S20 | 1     | S17 and S8  | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | ADJ | ON | 2006/11/02 15:00 |
| S21 | 14044 | 714/724 or 714/738 or 714/739<br>or 714/728 or 714/733 or 714/734<br>or 714/30 or 714/? | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | ADJ | ON | 2006/11/02 15:03 |
| S22 | 0     | S21 and S17   | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | ADJ | ON | 2006/11/02 15:03 |
| S23 | 0     | semiconductor device and S17  | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | ADJ | ON | 2006/11/02 15:04 |

## EAST Search History

10/892,724

| Ref # | Hits | Search Query  | DBs   | Default Operator | Plurals | Time Stamp       |
|-------|------|---|---|------------------|---------|------------------|
| S1    | 1    | (processor AND instruction register AND switchover device AND pseudorandom number generating device AND defined instruction AND undefined instruction AND normal operation AND test operation).clm. | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | ADJ              | ON      | 2006/11/02 13:34 |